Electrical Engineering	Candidates are strongly advised to look into the individual faculty member profiles before applying for the Ph. D Program. Ph.D written test AND Interview would be conducted on November 1, 2014 at PACL campus, IIT Indore. Candidates fulfilling the eligibility criterion mentioned in the advertisement should appear for the written test and Interview. Interview may be scheduled for next day (2 November, 2014) also. No separate email will be sent by the Electrical Engineering discipline to the candidates.
Dr. Shaibal Mukherjee	Photonics, Plasmonics, Nanoscale opto-electronics, Organic-inorganic nano-bio-electronics (for details, please visit "http://www.iiti.ac.in/people/~shaibal/index.htm").
Dr. Ram Bilas Pachori	1.Biomedical Signal Processing 2. Speech Signal Processing 3. Signal Processing for Communications 4. Time-Frequency Analysis For details , please visit : http://iiti.ac.in/people/~pachori/
Dr. Vimal Bhatia	1. Statistical and Adaptive Signal Processing 2. Channel Estimation and Equalization 3. Cooperative and Relay Communications 4. OFDM, 3/4G, MIMO Systems and Cognitive Radio For further details, please visit http://iiti.ac.in/people/~vbhatia/
Dr. Vivek	* Signal/Image processing and computer vision with focus on biometrics applications. For further details, please visit : http://iiti.ac.in/people/~kvivek/
Dr. Vipul Singh	Semiconductor device phyics, Organic electronics, Hybrid Functional Devices, Nanoelectronics, Bio/chemical Sensors, Photovoltaics, Single Electron Transistors(SETs), Silicon Based Nanodevices, Low Frequency Noise and ZnO based Nanostructures. For more details please visit: http://vipulsingh.synthasite.com

Dr. Abhinav Kranti	Solid-State Devices, VLSI, Circuit Design, Dynamic Memories, Nanotechnology and Biosensors. For more details please visit : (www.abhinavkranti.yolasite.com)
Dr. Santosh Kr. Vishvakarma	1. Investigation of Analog/RF and Digital Performance of Cylindrical Gate (ClyG) Gate-All-Around (GAA) Tunnel Field Effect Transistor (TFET) for Ultra Low Power Applications. 2. VLSI Circuit and System Design. 3. Graphene based Digital Circuit Design. For more details , please visit : http://iiti.ac.in/people/~skvishvakarma/